

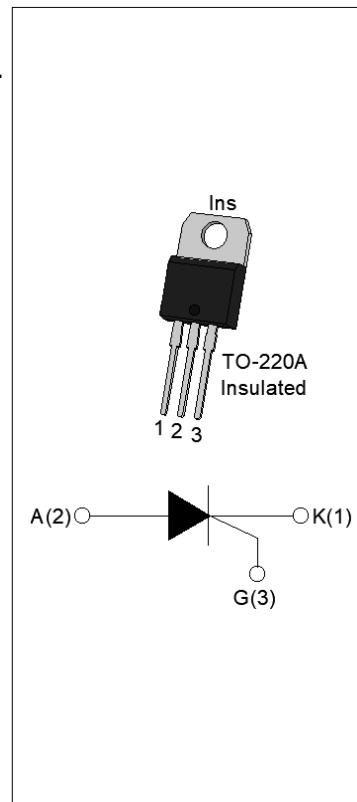


JCT612TA 12A SCR

Rev.A.1.0

DESCRIPTION:

JCT612TA silicon controlled rectifier is specifically designed for medium power switching and phase control applications. High current density due to mesa technology; SIPOS and Glass Passivation technology used has reliable operation up to 125°C junction temperature. Low I_{GT} parts available. From all three terminals to external heatsink, JCT612TA provides a rated insulation voltage of 2500 V_{RMS}, complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant.

**MAIN FEATURES**

Symbol	Value	Unit
$I_{T(RMS)}$	12	A
V_{DRM}/V_{RRM}	600	V
I_{GT}	≤ 5	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-125	°C
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	600	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	600	V
Average on-state current ($T_c \leq 89^\circ\text{C}$)	$I_{T(AV)}$	7.6	A
RMS on-state current ($T_c \leq 89^\circ\text{C}$)	$I_{T(RMS)}$	12	A
Non repetitive surge peak on-state current ($t_p=10\text{ms}, T_j=25^\circ\text{C}$)	I_{TSM}	140	A
Non repetitive surge peak on-state current ($t_p=8.3\text{ms}, T_j=25^\circ\text{C}$)		154	
I^2t value for fusing ($t_p=10\text{ms}, T_j=25^\circ\text{C}$)	I^2t	98	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}, f=100\text{Hz}, T_j=125^\circ\text{C}$)	di/dt	100	$\text{A}/\mu\text{s}$

Peak gate current ($t_p=20\mu s$, $T_j=125^\circ C$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ C$)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25^\circ C$; non-repetitive, off-state; FIG.7)	V_{pp}	0.5	kV

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ C$ unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	$V_D=12V$ $R_L=33\Omega$	-	-	5	mA
V_{GT}		-	-	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^\circ C$ $R_L=3.3K\Omega$	0.2	-	-	V
I_L	$I_G=1.2I_{GT}$	-	-	30	mA
I_H	$I_T=500mA$	-	-	15	mA
dV/dt	$V_D=400V$ Gate Open $T_j=125^\circ C$	400	-	-	V/ μs
t_{on}	$I_G=20mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25^\circ C$	-	5	-	μs
t_{off}		-	80	-	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=24A$	$t_p=380\mu s$	$T_j=25^\circ C$	1.5
V_{TO}	Threshold voltage		$T_j=125^\circ C$	0.8
R_D	Dynamic resistance		$T_j=125^\circ C$	$m\Omega$
I_{DRM}	$V_D=V_{DRM}$	$V_R=V_{RRM}$	$T_j=25^\circ C$	5
I_{RRM}			$T_j=125^\circ C$	0.2

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case(DC)	2.3	$^\circ C/W$
$R_{th(j-a)}$	junction to ambient (DC)	65	$^\circ C/W$

ORDERING INFORMATION

<u>J</u>	<u>CT</u>	<u>6</u>	<u>12</u>	<u>T</u>	<u>A</u>
JieJie Microelectronics Co.,Ltd.	SCRs				A:TO-220A(Ins)
		<u>6:V_{DRM} /V_{RRM} ≥600V</u>		<u>I_{T(RMS)}:12A</u>	<u>T:I_G≤5mA</u>

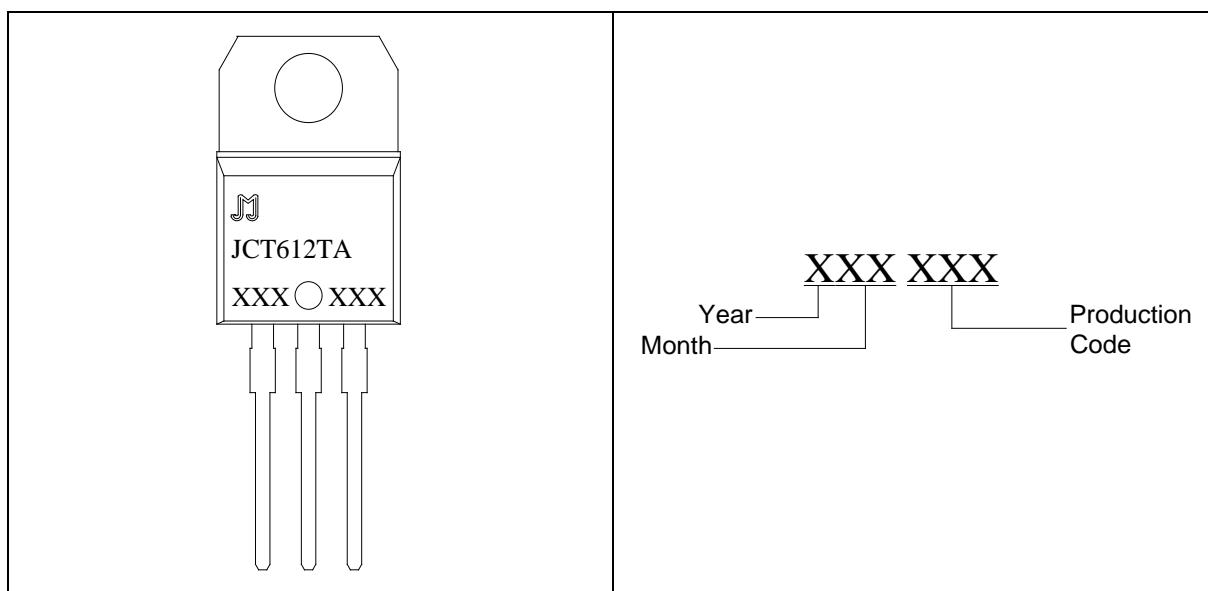
MARKING

FIG.1 Maximum power dissipation versus RMS on-state current

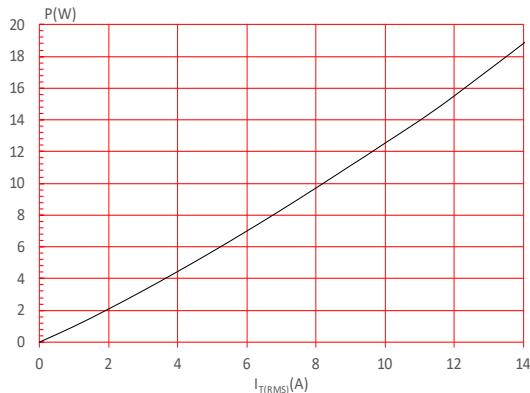


FIG.3: Surge peak on-state current versus number of cycles

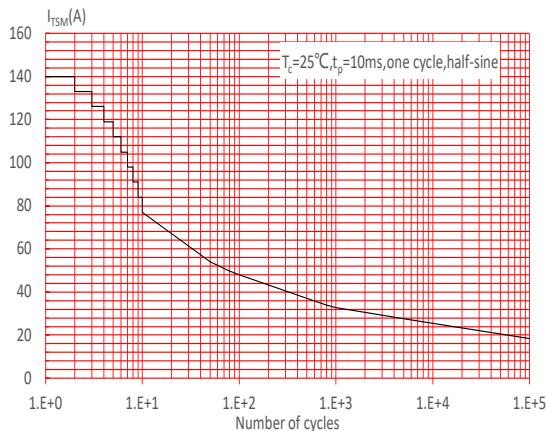


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$, and corresponding value of I^2t ($dI/dt < 100\text{A}/\mu\text{s}$)

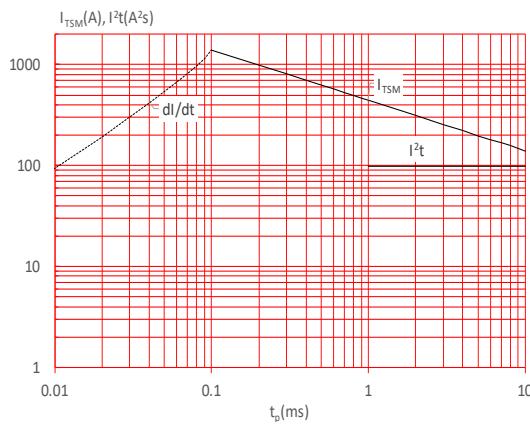


FIG.2: RMS on-state current versus case temperature

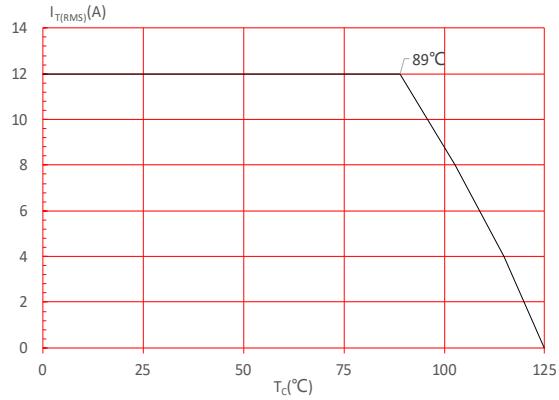


FIG.4: On-state characteristics

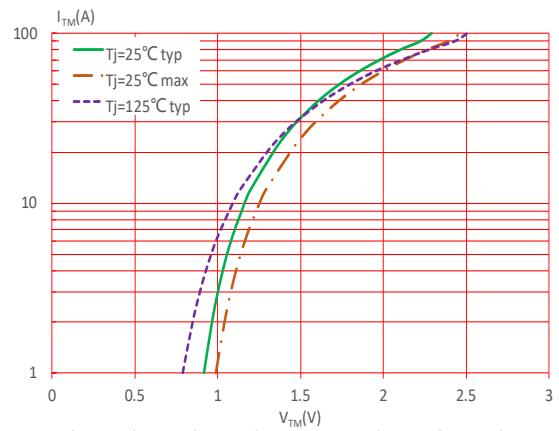


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

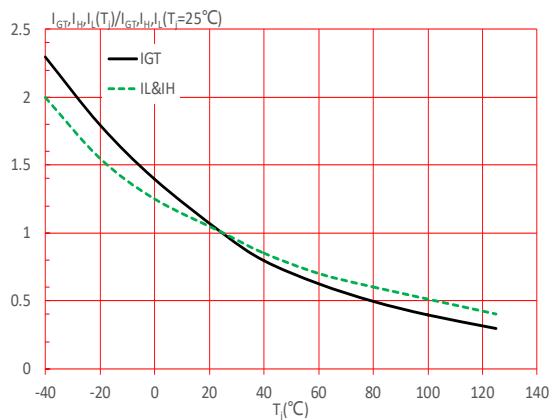
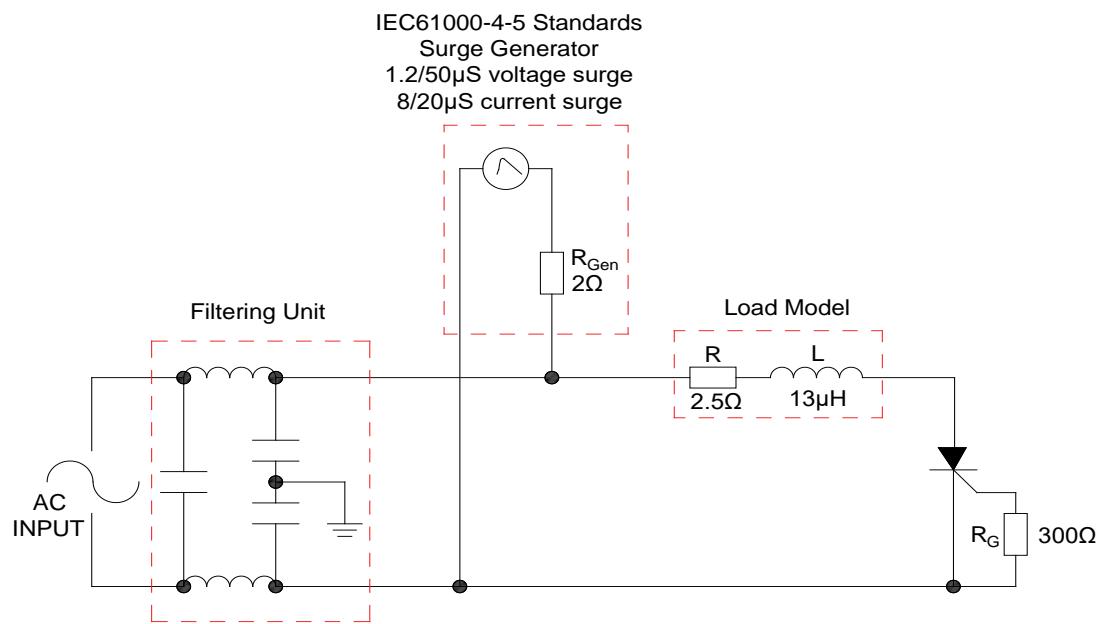


FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



SHAPING AND SOLDERING PARAMETERS

Refer to 《Instructions for installation of plastic-sealed in-line power devices》 released by JieJie

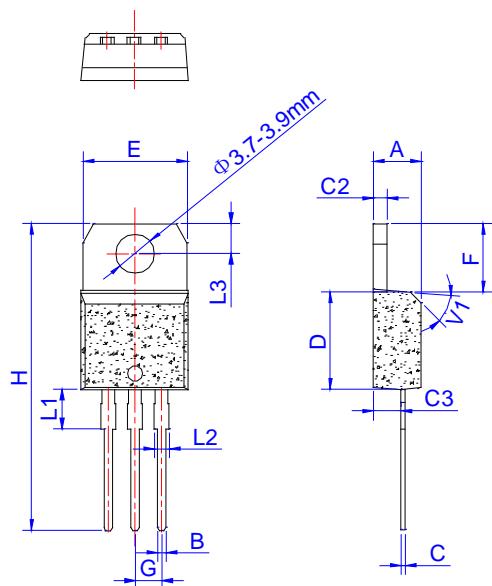
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT612TA	600	5	TO-220A(Ins)	50	Tube

Document Revision History

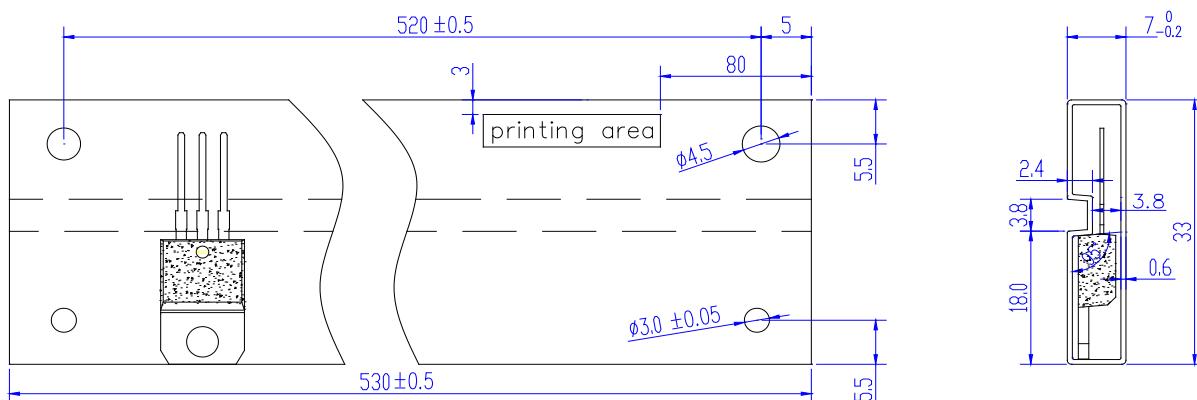
Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.80		10.4	0.386		0.409
F	6.25		6.85	0.246		0.270
G	2.40		2.70	0.094		0.106
H	28.0		29.8	1.102		1.173
L1	3.45		4.05	0.136		0.159
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

DELIVERY MODE



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220A	TUBE	50	1,000	5,000

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