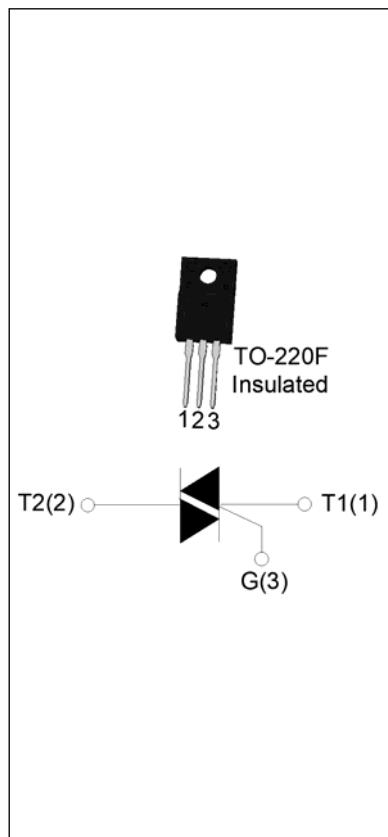


**DESCRIPTION:**

The JST23H-6F triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Compared to traditional triacs, JST23H-6F provides a very high switching capability up to junction temperatures of 150°C. By using an external plastic package, JST23H-6F provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.

**MAIN FEATURES**

Symbol	Value	Unit
$I_{T(RMS)}$	25	A
$V_{DRM}/V_{RRM}$	600	V
$I_{GT\text{ I/II/III}}$	50/50/50	mA

**ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Value	Unit
Storage junction temperature range	$T_{stg}$	-40-150	°C
Operating junction temperature range	$T_j$	-40-150	°C
Repetitive peak off-state voltage ( $T_j=25^\circ\text{C}$ )	$V_{DRM}$	600	V
Repetitive peak reverse voltage ( $T_j=25^\circ\text{C}$ )	$V_{RRM}$	600	V
RMS on-state current ( $T_c \leq 85^\circ\text{C}$ )	$I_{T(RMS)}$	25	A
Non repetitive surge peak on-state current (full cycle , $t_p=20\text{ms}$ , $T_j=25^\circ\text{C}$ )	$I_{TSM}$	250	A
Non repetitive surge peak on-state current (full cycle , $t_p=16.6\text{ms}$ , $T_j=25^\circ\text{C}$ )		275	
$I^2t$ value for fusing ( $t_p=10\text{ms}$ , $T_j=25^\circ\text{C}$ )	$I^2t$	310	$\text{A}^2\text{s}$
Critical rate of rise of on-state current ( $I_G=2 \times I_{GT}$ , $f=100\text{Hz}$ , $T_j=150^\circ\text{C}$ )	$dI/dt$	100	$\text{A}/\mu\text{s}$
Peak gate current ( $t_p=20\mu\text{s}$ , $T_j=150^\circ\text{C}$ )	$I_{GM}$	4	A

Average gate power dissipation ( $T_j=150^\circ\text{C}$ )	$P_{G(AV)}$	1	W
Peak gate power	$P_{GM}$	10	W
Peak pulse voltage ( $T_j=25^\circ\text{C}$ ; non-repetitive, off-state; FIG.7)	$V_{pp}$	2	kV

**ELECTRICAL CHARACTERISTICS** ( $T_j=25^\circ\text{C}$  unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
$I_{GT}$	$V_D = 12\text{V}$ $R_L = 33\Omega$	I - II - III	MAX.	50	mA
$V_{GT}$		I - II - III	MAX.	1	V
$V_{GD}$	$V_D = V_{DRM}$ $T_j = 150^\circ\text{C}$ $R_L = 3.3\text{k}\Omega$	I - II - III	MIN.	0.2	V
$I_L$	$I_G = 1.2I_{GT}$	I - III	MAX.	70	mA
		II		80	
$I_H$	$I_T = 500\text{mA}$		MAX.	60	mA
$dV/dt$	$V_D = 400\text{V}$ Gate Open $T_j = 150^\circ\text{C}$		MIN.	2200	V/ $\mu\text{s}$
$(dI/dt)c$	$(dV/dt)c = 20\text{V}/\mu\text{s}$ , $T_j = 150^\circ\text{C}$		MIN.	25	A/ms
$t_{on}$	$I_G = 80\text{mA}$ $I_A = 400\text{mA}$ $I_R = 40\text{mA}$ $T_j = 25^\circ\text{C}$	TYP.	15	$\mu\text{s}$	
$t_{off}$			100		

**STATIC CHARACTERISTICS**

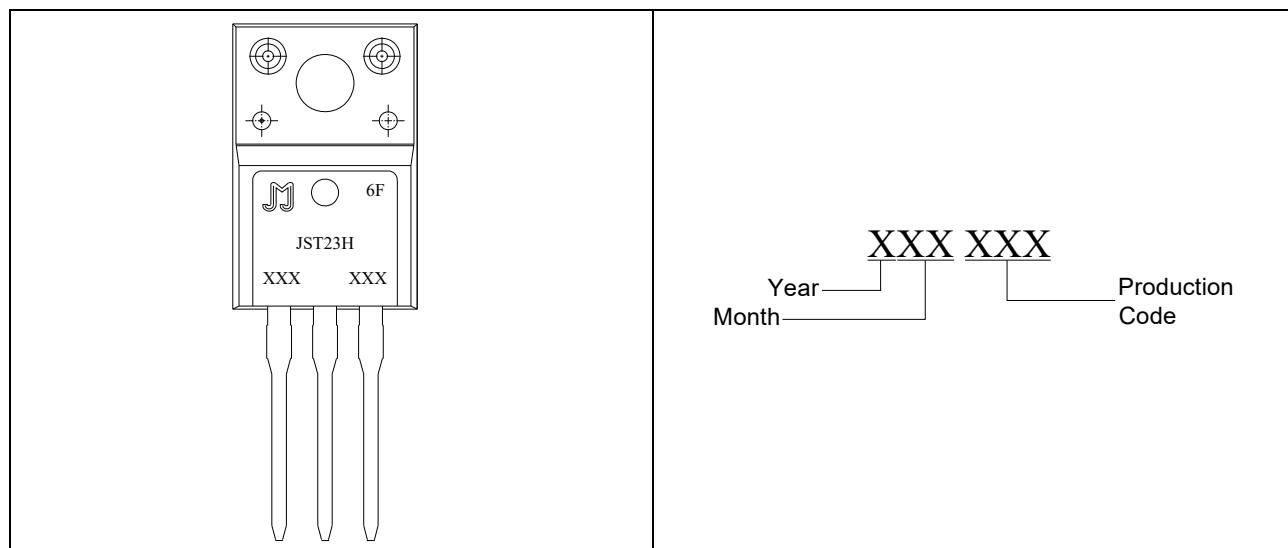
Symbol	Parameter		Value(MAX.)	Unit
$V_{TM}$	$I_{TM} = 35\text{A}$	$t_p = 380\mu\text{s}$	$T_j = 25^\circ\text{C}$	1.4
$V_{TO}$	Threshold voltage		$T_j = 150^\circ\text{C}$	0.72
$R_D$	Dynamic resistance		$T_j = 150^\circ\text{C}$	$\text{m}\Omega$
$I_{DRM}$	$V_D = V_{DRM}$	$V_R = V_{RRM}$	$T_j = 25^\circ\text{C}$	5
			$T_j = 150^\circ\text{C}$	4
$I_{RRM}$				mA

**THERMAL RESISTANCES**

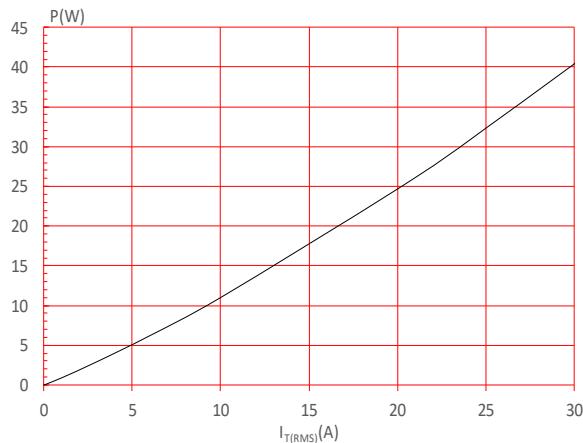
Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	2	$^\circ\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC)	60	$^\circ\text{C}/\text{W}$

**ORDERING INFORMATION**

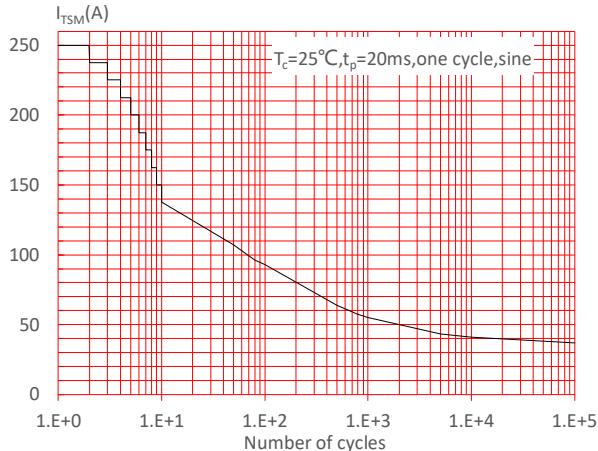
<b>J</b>	<b>ST</b>	<b>23</b>	<b>H</b>	<b>-6</b>	<b>F</b>
<u>JieJie Microelectronics Co.,Ltd.</u>					<u>F:TO-220F(Ins)</u>
	<u>Triacs</u>				
		<u>I<sub>T</sub>(RMS):25A</u>			
				<u>6:V<sub>DRM</sub> /V<sub>RRM</sub> ≥600V</u>	
			<u>High junction temperature</u>		

**MARKING**

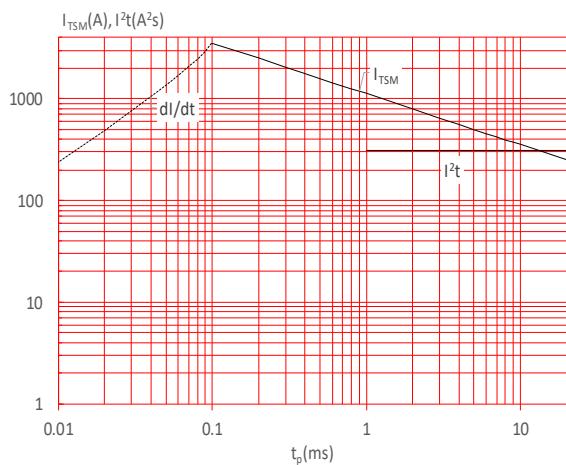
**FIG.1** Maximum power dissipation versus RMS on-state current



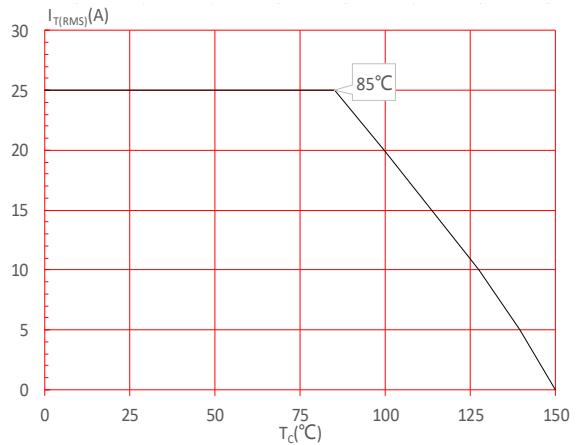
**FIG.3:** Surge peak on-state current versus number of cycles



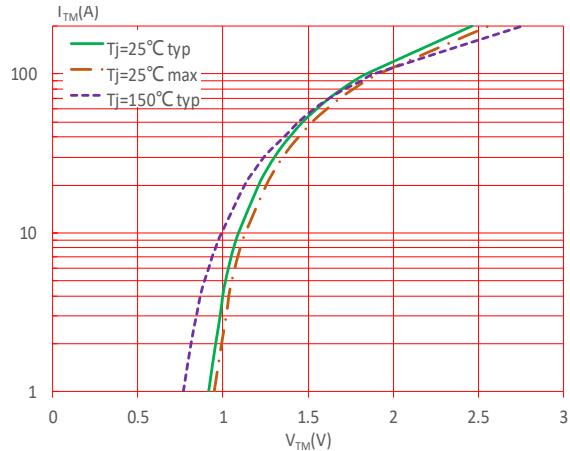
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20\text{ms}$ , and corresponding value of  $I^2t$  ( $dI/dt < 100\text{A}/\mu\text{s}$ )



**FIG.2:** RMS on-state current versus case temperature



**FIG.4:** On-state characteristics



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

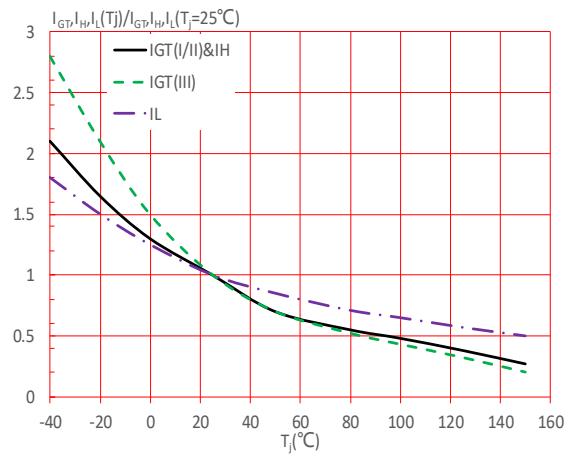
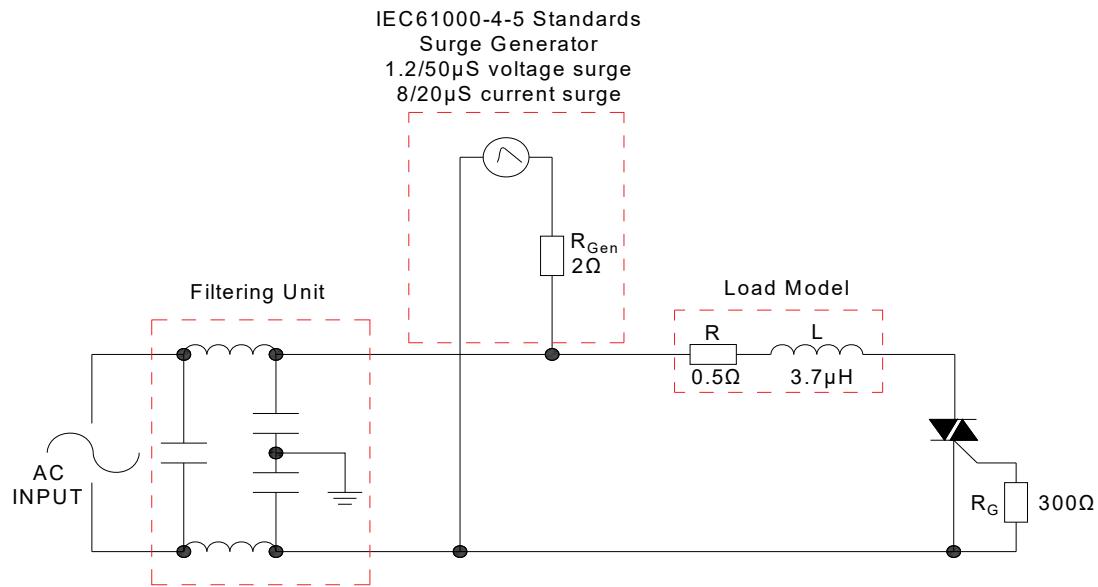


FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



## SHAPING AND SOLDERING PARAMETERS

Refer to 《Instructions for installation of plastic-sealed in-line power devices》 released by JieJie

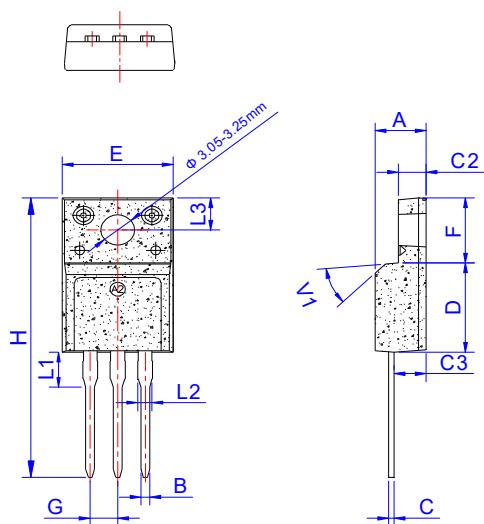
**ORDERING INFORMATION**

Order code	Voltage $V_{DRM}/V_{RRM}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		I -II-III			
<b>JST23H-6F</b>	<b>600</b>	<b>50</b>	<b>TO-220F(Ins)</b>	<b>50</b>	<b>Tube</b>

**Document Revision History**

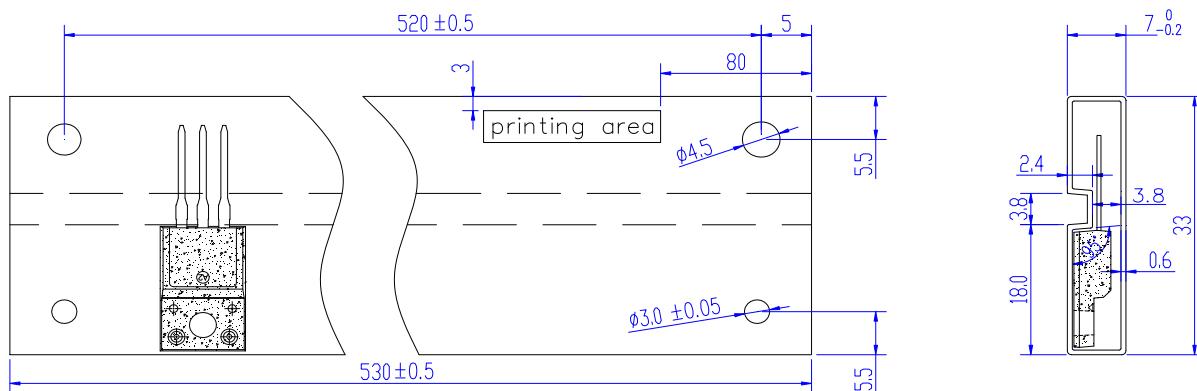
Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated

## PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.50		4.90	0.177		0.193
B	0.74	0.80	0.83	0.029	0.031	0.033
C	0.47		0.65	0.019		0.026
C2	2.45		2.75	0.096		0.108
C3	2.60		3.00	0.102		0.118
D	8.80		9.30	0.346		0.366
E	9.80		10.4	0.386		0.410
F	6.40		6.80	0.252		0.268
G	2.40		2.70	0.094		0.106
H	28.0		29.8	1.102		1.173
L1	3.20		3.80	0.126		0.150
L2	1.14		1.70	0.045		0.067
L3	3.20		3.60	0.126		0.142
V1		45°			45°	

## DELIVERY MODE



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220F	TUBE	50	1,000	5,000

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